

## Abstract

## Driver circuit

- 5     The invention relates to a driver circuit with
- a circuit node (10),
  - at least two first transistors (P1, P2), the load
- sections of which are switched in series and connect the
- 10    circuit node (10) with a first voltage ( $U_{10}$ ),
- at least two second transistors (N1, N2), the load
- sections of which are switched in series and connect the
- 15    circuit node (10) with a reference potential, and
- a control circuit (P3-P6, N3, D1-D4, R1, 16), which is
- formed in order to regulate at least a first control
- 20    voltage ( $U_{p2}$ ) on at least one transistor (P2) of the at
- least two first transistors (P1, P2) and at least a
- second control voltage ( $U_{N2}$ ) on at least one transistor
- (N2) of the at least two second transistors (N1, N2)
- dependent on a voltage at the circuit node (10).

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(Fig. 1)